TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHC04F,TC74VHC04FN,TC74VHC04FT,TC74VHC04FK

Hex Inverter

The TC74VHC04 is an advanced high speed CMOS INVERTER fabricated with silicon gate $\rm C^2MOS$ technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

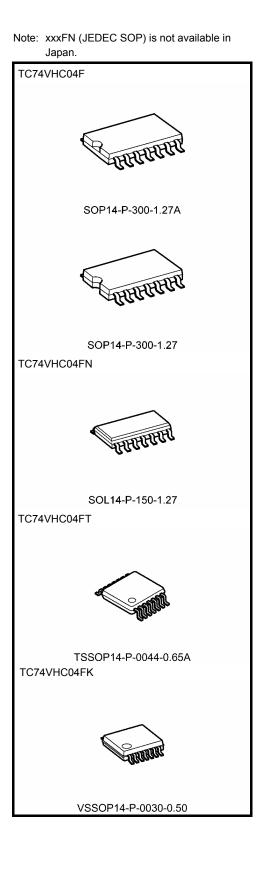
The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

Features

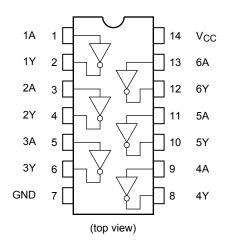
- High speed: $t_{pd} = 3.8 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 V to 5.5 V
- Low noise: $V_{OLP} = 0.8 V (max)$
- Pin and function compatible with 74ALS04

Weight	
SOP14-P-300-1.27A	: 0.18 g (typ.)
SOP14-P-300-1.27	: 0.18 g (typ.)
SOL14-P-150-1.27	: 0.12 g (typ.)
TSSOP14-P-0044-0.65A	: 0.06 g (typ.)
VSSOP14-P-0030-0.50	: 0.02 g (typ.)



<u>TOSHIBA</u>

Pin Assignment



IEC Logic Symbol

1A <u>(1)</u>	1	(<u>2)</u> 1Y
2A <u>(3)</u>		(4) 2Y
3A <u>(5)</u>		<u>(6)</u> 3Y
4A <u>(9)</u>		<u>(8)</u> 4Y
5A_(11)		(10) 5Y
6A_(13)		(<u>12)</u> 6Y

Truth Table

А	Y
L	Н
Н	L

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V_{CC} = 3.3 \pm 0.3 V)	ns/V
	uvuv	0 to 20 (V_{CC} = 5 \pm 0.5 V)	115/ V

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			7	Га = 25°()		a = 0 85°C	Unit
	,			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
High-level input voltage	V _{IH}		_	2.0 3.0 to 5.5	1.50 V _{CC} × 0.7	_	_	1.50 V _{CC} × 0.7	_	V
Low-level input voltage	VIL		_	2.0 3.0 to 5.5			0.50 V _{CC} × 0.3		0.50 V _{CC} × 0.3	v
High-level output voltage	Vон	VIN = VIL	$I_{OH} = -50 \ \mu A$ $I_{OH} = -4 \ m A$ $I_{OH} = -8 \ m A$	2.0 3.0 4.5 3.0 4.5	1.9 2.9 4.4 2.58 3.94	2.0 3.0 4.5 —		1.9 2.9 4.4 2.48 3.80		V
Low-level output voltage	V _{OL}	VIN = VIH	$I_{OL} = 50 \ \mu A$ $I_{OL} = 4 \ m A$ $I_{OL} = 8 \ m A$	2.0 3.0 4.5 3.0 4.5		0.0 0.0 0.0 —	0.1 0.1 0.1 0.36 0.36		0.1 0.1 0.1 0.44 0.44	V
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5			±0.1		±1.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	5.5			2.0	_	20.0	μΑ

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit								
	- ,		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max									
Propagation delay ^t pLH time ^t pHL		3.3 ± 0.3	15		5.0	7.1	1.0	8.5										
	t _{pLH}		5.5 ± 0.5	50		7.5	10.6	1.0	12.0	ns								
	t _{pHL}		5.0 ± 0.5	15		3.8	5.5	1.0	6.5									
		5.0 ± 0.5		5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	5.0 ± 0.5	50		5.3	7.5	1.0	8.5
Input capacitance	C _{IN}		_			4	10	_	10	pF								
Power dissipation capacitance	C _{PD}			(Note)	_	18			_	pF								

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

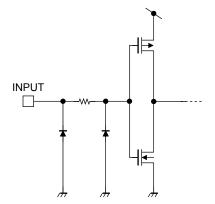
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition	-	Ta = 25°C		- Unit
Characteristics	Symbol		V _{CC} (V)	Тур.	Limit	Offic
Quiet output maximum dynamic V_{OL}	V _{OLP}	$C_L = 50 \text{ pF}$	5.0	0.4	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.4	-0.8	V
Minimum high level dynamic input voltage	VIHD	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0		1.5	V

Input Equivalent Circuit

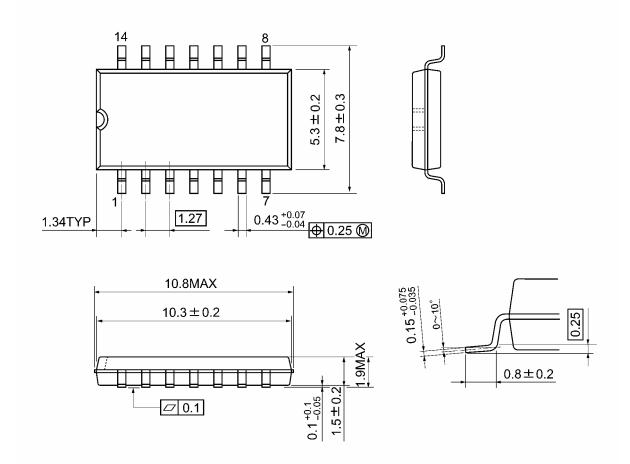




Package Dimensions

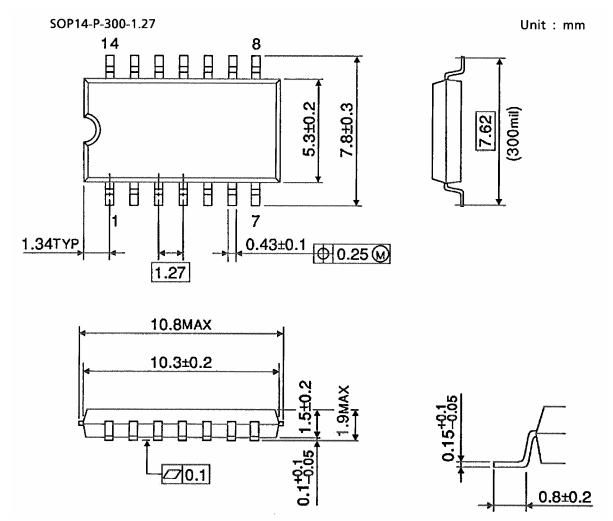
SOP14-P-300-1.27A

Unit: mm



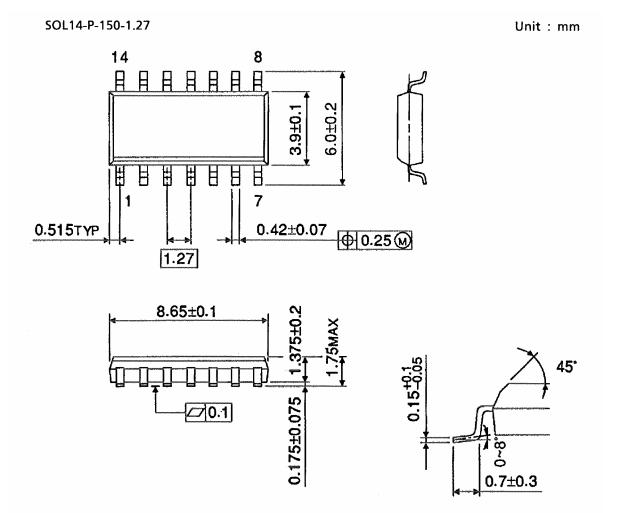
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)



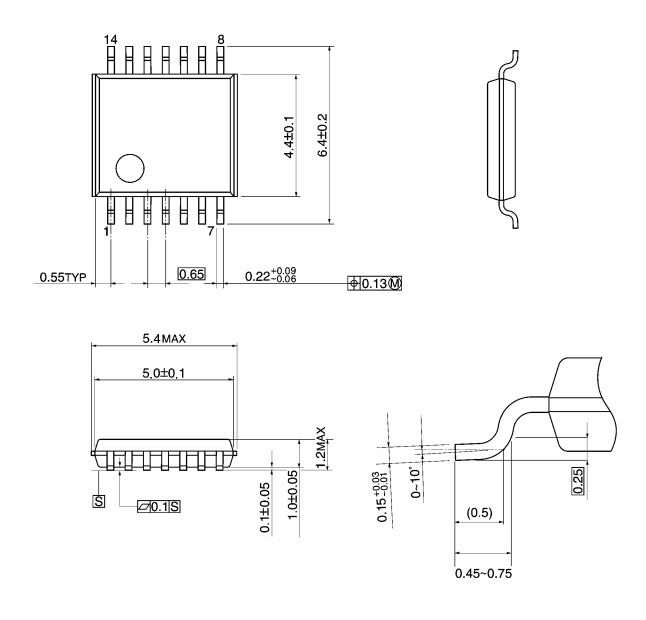
Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



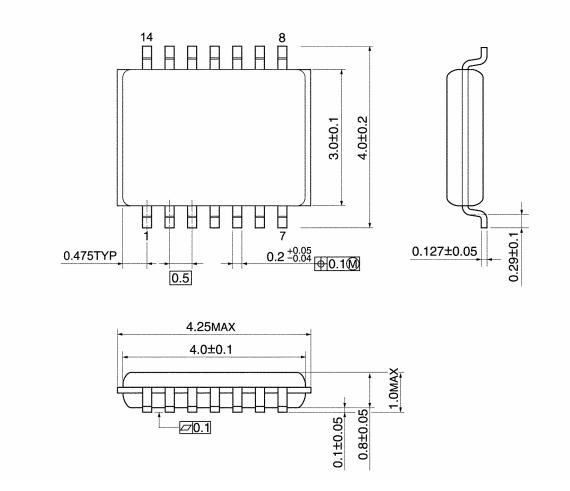
Weight: 0.06 g (typ.)

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Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A VSSOP14-P-0030-0.50

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